



Diodes Incorporated Discrete and Analog Semiconductors

Qualification Report - PCN-2236

Manufacturer No.: Die Attach Material and Back Metal Composition changes to

improve product performance

Revision: 0

Date: June 27, 2016

Qualified By: Diodes Incorporated

Also Applicable To: The part numbers listed in the associated PCN are Qualified by

Similarity (QBS) to the devices included in this report.

Please go to www.diodes.com for current data sheets on

associated devices

Prepared By: Diodes US Document Control Date June 27, 2016

Approved By: Diodes US QRA Department Date June 27, 2016







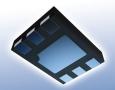


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Quality and reliability data provided by Diodes Incorporated is intended to be an estimate of product performance based upon history only. It does not imply that any performance levels reflected in such data can be met if the product is operated outside the conditions expressly stated in the latest published data sheet for a device.

Existing industry standards for plastic encapsulated microcircuit qualification and reliability monitors are based upon historical data, experiments, and field experience with the use of these devices in commercial and industrial applications. The applicability of these standards in determining the suitability for use and safety performance in life support, military and aerospace applications has not been established. Due to the multiple variations in field operating conditions, a component manufacturer can only base estimates of product life on models and the results of package and die level qualification. The buyer's use of this data, and all consequences of such use, is solely the buyer's responsibility. Buyer assumes full responsibility to perform sufficient engineering and additional qualification testing in order to properly evaluate the buyer's application and determine whether a candidate device is suitable for use in that application. The information provided by Diodes Incorporated shall not be considered sufficient grounds on which to base any such determination.

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DIODES INCORPORATED

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DATE: 27th June, 2016

PCN #: 2236

PCN Title: Die Attach Material and Back Metal Composition changes to improve

product performance

Dear Customer:

This is an announcement of change(s) to products that are currently being offered by Diodes Incorporated.

We request that you acknowledge receipt of this notification within 30 days of the date of this PCN. If you require samples for evaluation purposes, please make a request within 30 days as well. Otherwise, samples may not be built prior to this change. Please refer to the implementation date of this change as it is stated in the attached PCN form. Please contact your local Diodes sales representative to acknowledge receipt of this PCN and for any sample requests.

The changes announced in this PCN will not be implemented earlier than 90 days from the notification date stated in the attached PCN form.

Previously agreed upon customer specific change process requirements or device specific requirements will be addressed separately.

For questions or clarification regarding this PCN, please contact your local Diodes sales representative.

Sincerely,

Diodes Incorporated PCN Team



PRODUCT CHANGE NOTICE

PCN-2236 REV 00

Notification Date:	Implementation Date:	Product Family:	Change Type:	PCN #:	
27th June, 2016	26th September, 2016	Discrete Products	Die Attach Materials / Back Metal Composition	2236	
		TITLE			

Die Attach Material and Back Metal Composition changes to improve product performance

DESCRIPTION OF CHANGE

This PCN is being issued to notify customers that in order to improve product performance, Diodes Incorporated has qualified an enhanced die attach material and back metal composition for the part number(s) listed in this PCN.

Full electrical characterization and reliability testing have been completed to ensure that no changes in product reliability, device functionality or data sheet electrical specifications exist.

There will be no change to the Form, Fit, or Function of affected products.

No change in datasheet parameters PRODUCTS AFFECTED

AL5802-7, AL5802-13, ASMCC0164-7

WEB LINKS				
Manufacturer's Notice:	http://www.diodes.com/quality/pcns			
For More Information Contact:	http://www.diodes.com/contacts			
Data Sheet:	http://www.diodes.com/products			
DISCI AIMED				

DISCLAIMER

Unless a Diodes Incorporated Sales representative is contacted in writing within 30 days of the posting of this notice, all changes described in this announcement are considered approved.

Certificate of Design, Construction & Qualification



Description: Qualification of ASMCC0164 and AL5802

	Category					QBS Source Device 1		Qual Device 1	
	Product	Part Number				ASMCC0164-7		AL5802-7	
	Assembly	Package Type				SOT-26		SOT-26	
	Assembly	Package Size				3.0*2.8*1.15 mm		3.0*2.8*1.15 mm	
	Wafer	Die Name(s)				C3904EG + DB-ICT036N140S15		C3904EG + DB-ICT036N140S15	
	Wafer	Die Size (W/L/Thickness) - After Saw				C3904EG: 0.4*0.31*0.216 mm DB-ICT036N140S15:		C3904EG: 0.4*0.31*0.216 mm DB-ICT036N140S15:	
	Walci	Die Size (W/E/Tillekliess) Arter saw				0.36*0.36*0.15 mm		0.36*0.36*0.15 mm	
	Wafer	Die Process / Technology				Bipolar		Bipolar	
	Wafer	Wafer FAB/ Location				KFAB/USA & Phenitec/Japan		KFAB/USA & Phenitec/Japan	
	Water	water raby Escation							
	Wafer	Wafer Diameter				6" for C3904EG and 5" for DB-		6" for C3904EG and 5" for DB-	
	Wafer	Front Metal Type				ICT036N140S15 Al		ICT036N140S15 Al	
						C3904EG: 3.5 um DB-		C3904EG: 3.5 um DB-	
	Wafer	Front Metal Layer Number/ Thickness				ICT036N140S15: 2.0 um		ICT036N140S15: 2.0 um	
						C3904EG: Ti/Ni/Au DB-		C2004FC, T: /N: /A	
	Wafer	Back Metal Type (All Layers)				C3904EG: Ti/Ni/Au DB- ICT036N140S15: AuAs-Au		C3904EG: Ti/Ni/Au DB-ICT036N140S15: AuAs-Au	
						101030111103131710713710		22 101030112 100231710713710	
						C3904EG: 1500A Ti + 3300A		C3904EG: 1500A Ti + 3300A	
	Wafer	Back Metal Thickness (All Layers)				NiV + 600A Au DB-		NiV + 600A Au DB-	
						ICT036N140S15: 0.9 um		ICT036N140S15: 0.9 um	
	Wafer	Die Conforming Coating (Passivation)				C3904EG: Oxide + Nitride DB-		C3904EG: Oxide + Nitride DB-	
	Water	Die Comorning Coating (1 assivation)				ICT036N140S15: N/A		ICT036N140S15: N/A	
						C3904EG: 6kA+/-600A Oxide		C3904EG: 6kA+/-600A Oxide	
	Wafer	Die passivation thickness range				and 1.5kA+/-200A Nitride DB-		and 1.5kA+/-200A Nitride DB-	
						ICT036N140S15: N/A		ICT036N140S15: N/A	
	Mofor	No of marks Stone				C3904EG: 5		C3904EG: 5	
	Wafer	No of masks Steps				DB036N140D15CH: N/A		DB036N140D15CH: N/A	
	Assembly	Die quantity per package (e.g. single or dual dies)		ĺ		Dual		Dual	
	Assembly Assembly	Die Attach Method (DB Epoxy/Solder Type) Die Attach Material/ Supplier				Epoxy 84-1LMSR4		Epoxy 84-1LMSR4	
	Assembly	Bond Wire/Clip Bond Material/ Supplier				Au		Au Au	
	Assembly	Bond Type (at Die)				Thermo-Ultrasonic		Thermo-Ultrasonic	
	Assembly	Bond Type (at LF)		ĺ		Thermo-Ultrasonic		Thermo-Ultrasonic	
	Assembly	No. of bond over active area				5		5	
	Assembly	Glass Transistion Temp				130°C		130°C	
	Assembly Assembly	Terminal Finish (Plating) Material Header plating (Die Land Area)				100% Matte Tin Silver Spot Plating		100% Matte Tin Silver Spot Plating	
	Assembly	Wire Diameter				1.0 mil		1.0 mil	
	Assembly	Leadframe Type				SOT-26Z		SOT-26Z	
	Assembly	Leadframe Material				EFTEC-64T		EFTEC-64T	
	Assembly	Lead Frame Manufacturer				MHT		MHT	
	Assembly	Molding Compound Type				CEL1700HF40SK-D3		CEL1700HF40SK-D3	
	Assembly	Mold Compound Material Manufacturer Green Compound (Yes/No)		ĺ		Hitachi Ves		Hitachi Ves	
	Assembly Assembly	Lead-Free (Yes/No)				Yes Yes		Yes Yes	
	Assembly	Assembly Site/ Location				SAT/Shanghai		SAT/Shanghai	
	Assembly	Test Site/ Location				SAT/Shanghai		SAT/Shanghai	
	Product	Max Junction Temp				150°C		150°C	
	Product	Max Thermal resistance Junc (case)				70 °C/W		70 °C/W	
	Product								
	Drodust	Max Thermal resistance Junc (amibent)				200 °C/W		200 °C/W	
	Product	DataSheet				200 °C/W DS31781 Rev. 3 - 5		200 °C/W DS35516 Rev. 9 - 2	
								·	
# in AEC- Q101		DataSheet	Duration / Limits	Accept on # Failed/ Sample	# of Lots		Results Pass/Fail	·	Results Pass/Fail
AEC-		DataSheet Reliability and Characterization Testing		# Failed/		DS31781 Rev. 3 - 5		DS35516 Rev. 9 - 2	
AEC- Q101		DataSheet Reliability and Characterization Testing Test Conditions	Duration / Limits	# Failed/ Sample		DS31781 Rev. 3 - 5 X = Test Needed	Pass/Fail	DS35516 Rev. 9 - 2 QBS Test Completed	Pass/Fail
AEC- Q101	Test	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C	Duration / Limits 24 Hrs	# Failed/ Sample		DS31781 Rev. 3 - 5 X = Test Needed X	Pass/Fail Pass	QBS Test Completed QBS	Pass/Fail Pass
AEC- Q101	Test MSL1 Pre-	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH	Duration / Limits	# Failed/ Sample Size per Lot SMD only, for Test #7,		X = Test Needed X X X	Pass/Fail	QBS Test Completed QBS QBS QBS	Pass/Fail
AEC- Q101 (D)	Test	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C	Duration / Limits 24 Hrs	# Failed/ Sample Size per Lot SMD only,		DS31781 Rev. 3 - 5 X = Test Needed X	Pass/Fail Pass	QBS Test Completed QBS	Pass/Fail Pass
AEC- Q101 (D)	Test MSL1 Pre- conditioning	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH	Duration / Limits 24 Hrs 168Hrs	# Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10	3 Assembly lots	X = Test Needed X X X	Pass/Fail Pass Pass	QBS Test Completed QBS QBS QBS	Pass/Fail Pass Pass
AEC- Q101 (D)	Test MSL1 Pre- conditioning	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH	Duration / Limits 24 Hrs 168Hrs	# Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10	3 Assembly lots	X = Test Needed X X X	Pass/Fail Pass Pass	QBS Test Completed QBS QBS QBS	Pass/Fail Pass Pass
AEC- Q101 (D)	Test MSL1 Pre- conditioning	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C	Duration / Limits 24 Hrs 168Hrs 3 cycles	# Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10	3 Assembly lots	X = Test Needed X = X X	Pass/Fail Pass Pass Pass	QBS Test Completed QBS QBS QBS QBS	Pass/Fail Pass Pass Pass
AEC- Q101 (D)	Test MSL1 Pre- conditioning	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071	Duration / Limits 24 Hrs 168Hrs 3 cycles	#Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10	3 Assembly lots tion parts submitted for testing	DS31781 Rev. 3 - 5 X = Test Needed X X X X	Pass/Fail Pass Pass Pass Pass	QBS Test Completed QBS QBS QBS QBS QBS QBS	Pass/Fail Pass Pass Pass Pass
AEC- Q101 (D)	MSL1 Pre- conditioning EXTERNAL VISUAL (EV)	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC	# Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10	3 Assembly lots	X = Test Needed X = X X	Pass/Fail Pass Pass Pass	QBS Test Completed QBS QBS QBS QBS	Pass/Fail Pass Pass Pass
AEC- Q101 (D)	Test MSL1 Pre- conditioning EXTERNAL VISUAL (EV) PARAMETRIC	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range,	#Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10	3 Assembly lots tion parts submitted for testing	DS31781 Rev. 3 - 5 X = Test Needed X X X X	Pass/Fail Pass Pass Pass Pass	QBS Test Completed QBS QBS QBS QBS QBS QBS	Pass/Fail Pass Pass Pass Pass
AEC- Q101 (D)	Test MSL1 Pre- conditioning EXTERNAL VISUAL (EV) PARAMETRIC	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs	#Failed/Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualification 0/25 0/77 0/77	3 Assembly lots tion parts submitted for testing	X = Test Needed X = X X X X X X	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass Pass Pass Pass Pass Pass
AEC-Q101 (D) 2 3	MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV)	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs	# Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77	3 Assembly lots tion parts submitted for testing 3 wafer lots	X = Test Needed X = X X X X X X X X X X X X X	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass
AEC-Q101 (D) 2 3 4 5	Test MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles	# Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77	3 Assembly lots ion parts submitted for testing 3 wafer lots 3 wafer lots	X = Test Needed X = X X X X X X X X X X X X X	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass
AEC-Q101 (D) 2 3	MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV)	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs	# Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77	3 Assembly lots tion parts submitted for testing 3 wafer lots	X = Test Needed X = X X X X X X X X X X X X X	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass
AEC-Q101 (D) 2 3 4 5	MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 500 Cycles 1000 Cycles	# Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77	3 Assembly lots tion parts submitted for testing 3 wafer lots 3 wafer lots 3 Assembly lots	X = Test Needed X = Test Needed X X X X X X X X X X X X X X X X X	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass
AEC-Q101 (D) 2 3 4 5	Test MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 500 Cycles 1000 Cycles 96 Hrs	# Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77 0/77	3 Assembly lots ion parts submitted for testing 3 wafer lots 3 wafer lots	X = Test Needed X = Test Needed X X X X X X X X X X X X X X X X X	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass
AEC-Q101 (D) 2 3 4 5 7	Test MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB TC PCT/AC	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-A102 Ta=85°C, 85% RH, with 80% Maximum Reverse	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 500 Cycles 1000 Cycles 96 Hrs 168 Hrs	# Failed/Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77 0/77 0/77 0/77	3 Assembly lots tion parts submitted for testing 3 wafer lots 3 wafer lots 3 Assembly lots 3 Assembly lots	X = Test Needed X = Test Needed X X X X X X X X X X X X X X X X X	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass
AEC-Q101 (D) 2 3 4 5	MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-A102	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 500 Cycles 1000 Cycles 96 Hrs	# Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77 0/77	3 Assembly lots tion parts submitted for testing 3 wafer lots 3 wafer lots 3 Assembly lots	X = Test Needed X = Test Needed X X X X X X X X X X X X X X X X X	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass
AEC-Q101 (D) 2 3 4 5 7 8 9 alt	Test MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB TC PCT/AC H3TRB	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-A102 Ta=85°C, 85% RH, with 80% Maximum Reverse Bias. JESD22A-101	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 1000 Cycles 1000 Cycles 96 Hrs 168 Hrs 500 Hrs 1000 Hrs 2520 Cycles	# Failed/Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77	3 Assembly lots cion parts submitted for testing 3 wafer lots 3 wafer lots 3 Assembly lots 3 Assembly lots 3 wafer lots	X = Test Needed X = Test Needed X X X X X X X X X X X X X X X X X	Pass/Fail Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass
AEC-Q101 (D) 2 3 4 5 7	Test MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB TC PCT/AC	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-A102 Ta=85°C, 85% RH, with 80% Maximum Reverse	24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 500 Cycles 1000 Cycles 1000 Hrs 168 Hrs 500 Hrs 158 Cycles 500 Cycles 1000 Cycles	# Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77	3 Assembly lots tion parts submitted for testing 3 wafer lots 3 wafer lots 3 Assembly lots 3 Assembly lots	X = Test Needed X = Test Needed X X X X X X X X X X X X X X X X X	Pass/Fail Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass Pass Pass
AEC-Q101 (D) 2 3 4 5 7 8 9 alt	Test MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB TC PCT/AC H3TRB	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-A102 Ta=85°C, 85% RH, with 80% Maximum Reverse Bias. JESD22A-101 MIL-STD-750 Method 1037 (N/A for TVS)	24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 500 Cycles 1000 Cycles 1000 Hrs 168 Hrs 500 Hrs 1500 Hrs 1500 Cycles 15000 Cycles	# Failed/Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77	3 Assembly lots cion parts submitted for testing 3 wafer lots 3 Assembly lots 3 Assembly lots 3 wafer lots 3 wafer lots	X = Test Needed X = Test Needed X X X X X X X X X X X X X X X X X	Pass/Fail Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass Pass Pass
AEC-Q101 (D) 2 3 4 5 7 8 9 alt	Test MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB TC PCT/AC H3TRB	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-A102 Ta=85°C, 85% RH, with 80% Maximum Reverse Bias. JESD22A-101	24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 500 Cycles 1000 Cycles 1000 Hrs 168 Hrs 500 Hrs 158 Cycles 500 Cycles 1000 Cycles	# Failed/ Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77	3 Assembly lots cion parts submitted for testing 3 wafer lots 3 wafer lots 3 Assembly lots 3 Assembly lots 3 wafer lots	X = Test Needed X = Test Needed X X X X X X X X X X X X X X X X X	Pass/Fail Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass
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AEC-Q101 (D) 2 3 4 5 7 8 9 alt 10 11 12 13	Test MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB TC PCT/AC H3TRB IOL ESD DPA Package Physical	DataSheet Reliability and Characterization Testing Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-A102 Ta=85°C, 85% RH, with 80% Maximum Reverse Bias. JESD22A-101 MIL-STD-750 Method 1037 (N/A for TVS) HBM (AEC-Q101-001) CDM (AEC-Q100-005) AEC Q101-004 SEC. 4 JESD22-B100	24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Cycles 1000 Cycles 1000 Hrs 168 Hrs 500 Hrs 1000 Hrs 168 Hrs 500 Hrs 1000 Cycles 1000 Cycles PER DATA SHEET PER DATA SHEET	# Failed/Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/30 0/30 0/2 0/30	3 Assembly lots ion parts submitted for testing 3 wafer lots 3 wafer lots 3 Assembly lots 3 wafer lots 3 wafer lots 1 wafer lots 1 wafer lot 1 Assembly lot 1 Assembly lot	X	Pass/Fail Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass Pass Pass
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AEC-Q101 (D) 2 3 4 5 7 8 9 alt 10 11 12 13 20 21 22 23	Test MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB TC PCT/AC H3TRB IOL ESD DPA Package Physical Dimemsions (PD) RESISTANCE TO SOLDER HEAT (RSH) Solderability THERMAL RESISTANCE (TR) Wire Bond Strength	DataSheet Reliability and Characterization Testing Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-A102 Ta=85°C, 85% RH, with 80% Maximum Reverse Bias. JESD22A-101 MIL-STD-750 Method 1037 (N/A for TVS) HBM (AEC-Q101-001) CDM (AEC-Q100-005) AEC Q101-004 SEC. 4 JESD22-B100 JESD22 A-111 (SMD), B-106 (PTH) (260C @30S) J-STD-002; JESD22B102 (245C +0/5S) JESD 24-3, 24-4, 24-6 AS APPROPRIATE MIL-STD-750 METHOD 2037 (JESD22-B116B)	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 500 Cycles 1000 Cycles 1000 Hrs 168 Hrs 500 Hrs 1000 Hrs 2520 Cycles 15000 Cycles PER DATA SHEET PER DATA SHEET PER DATA SHEET PER SPEC 5 Seconds PER SPEC Cpk>1.66	# Failed/Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/70 0/30 0/30 0/2 0/30 0/10 0/10 0/ min of 5	3 Assembly lots ition parts submitted for testing 3 wafer lots 3 wafer lots 3 Assembly lots 3 Assembly lots 3 wafer lots 3 wafer lots 1 wafer lot 1 wafer lot 1 Assembly lot	X = Test Needed X	Pass/Fail Pass Pass Pass Pass Pass	QBS Test Completed QBS QBS QBS QBS QBS QBS QBS QBS QBS QB	Pass/Fail Pass Pass
AEC-Q101 (D) 2 3 4 5 7 8 9 alt 10 11 12 13 20 21 22	Test MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB TC PCT/AC H3TRB IOL ESD DPA Package Physical Dimemsions (PD) RESISTANCE TO SOLDER HEAT (RSH) Solderability THERMAL RESISTANCE (TR) Wire Bond Strength BOND SHEAR	DataSheet Reliability and Characterization Testing Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-A102 Ta=85°C, 85% RH, with 80% Maximum Reverse Bias. JESD22A-101 MIL-STD-750 Method 1037 (N/A for TVS) HBM (AEC-Q101-001) CDM (AEC-Q100-005) AEC Q101-004 SEC. 4 JESD22-B100 JESD22 A-111 (SMD), B-106 (PTH) (260C @305) J-STD-002; JESD22B102 (245C +0/5S) JESD 24-3, 24-4, 24-6 AS APPROPRIATE MIL-STD-750 METHOD 2037 (JESD22-B116B) AEC-Q101-003	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 500 Cycles 1000 Cycles 1000 Hrs 168 Hrs 500 Hrs 1000 Hrs 2520 Cycles 15000 Cycles 7560 Cycles PER DATA SHEET PER DATA SHEET PER DATA SHEET PER DATA SHEET PER SPEC 5 Seconds PER SPEC Cpk>1.66 Cpk>1.66	# Failed/Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/70 0/30 0/30 0/30 0/30 0/10 0/10 0/ min of 5 0/ min of 5	3 Assembly lots stion parts submitted for testing 3 wafer lots 3 wafer lots 3 Assembly lots 3 wafer lots 3 wafer lots 3 wafer lots 1 wafer lot 1 wafer lot 1 Assembly lot	X = Test Needed X	Pass/Fail Pass Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass
AEC-Q101 (D) 2 3 4 5 7 8 9 alt 10 11 12 13 20 21 22 23 24	Test MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB TC PCT/AC H3TRB IOL ESD DPA Package Physical Dimemsions (PD) RESISTANCE TO SOLDER HEAT (RSH) Solderability THERMAL RESISTANCE (TR) Wire Bond Strength	DataSheet Reliability and Characterization Testing Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-A102 Ta=85°C, 85% RH, with 80% Maximum Reverse Bias. JESD22A-101 MIL-STD-750 Method 1037 (N/A for TVS) HBM (AEC-Q101-001) CDM (AEC-Q100-005) AEC Q101-004 SEC. 4 JESD22-B100 JESD22 A-111 (SMD), B-106 (PTH) (260C @30S) J-STD-002; JESD22B102 (245C +0/5S) JESD 24-3, 24-4, 24-6 AS APPROPRIATE MIL-STD-750 METHOD 2037 (JESD22-B116B)	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 500 Cycles 1000 Cycles 1000 Hrs 168 Hrs 500 Hrs 1000 Hrs 2520 Cycles 15000 Cycles PER DATA SHEET PER DATA SHEET PER DATA SHEET PER SPEC 5 Seconds PER SPEC Cpk>1.66	# Failed/Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/70 0/30 0/30 0/2 0/30 0/10 0/10 0/ min of 5	3 Assembly lots ition parts submitted for testing 3 wafer lots 3 wafer lots 3 Assembly lots 3 Assembly lots 3 wafer lots 3 wafer lots 1 wafer lot 1 wafer lot 1 Assembly lot	X = Test Needed X X X X X X X X X	Pass/Fail Pass Pass Pass Pass Pass	QBS Test Completed QBS QBS QBS QBS QBS QBS QBS QBS QBS QB	Pass/Fail Pass
AEC-Q101 (D) 2 3 4 5 7 8 9 alt 10 11 12 13 20 21 22 23 24	MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB TC PCT/AC H3TRB IOL ESD DPA Package Physical Dimemsions (PD) RESISTANCE TO SOLDER HEAT (RSH) Solderability THERMAL RESISTANCE (TR) Wire Bond Strength BOND SHEAR Die Shear Summary: Submitted By:	Test Conditions Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-A102 Ta=85°C, 85% RH, with 80% Maximum Reverse Bias. JESD22A-101 MIL-STD-750 Method 1037 (N/A for TVS) HBM (AEC-Q101-001) CDM (AEC-Q100-005) AEC Q101-004 SEC. 4 JESD22-B100 JESD22 A-111 (SMD), B-106 (PTH) (260C @30S) J-STD-002; JESD22B102 (245C +0/5S) JESD 24-3, 24-4, 24-6 AS APPROPRIATE MIL-STD-750 METHOD 2037 (JESD22-B116B) AEC-Q101-003 MIL-STD-750 (2017) Yuan Dai, 02/02/2016	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 500 Cycles 1000 Cycles 1000 Hrs 168 Hrs 500 Hrs 1000 Hrs 2520 Cycles 15000 Cycles 7560 Cycles PER DATA SHEET PER DATA SHEET PER DATA SHEET PER DATA SHEET PER SPEC 5 Seconds PER SPEC Cpk>1.66 Cpk>1.66	# Failed/Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/70 0/30 0/30 0/30 0/30 0/10 0/10 0/ min of 5 0/ min of 5	3 Assembly lots stion parts submitted for testing 3 wafer lots 3 wafer lots 3 Assembly lots 3 wafer lots 3 wafer lots 3 wafer lots 1 wafer lot 1 wafer lot 1 Assembly lot	X = Test Needed X X X X X X X X X	Pass/Fail Pass Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass
AEC-Q101 (D) 2 3 4 5 7 8 9 alt 10 11 12 13 20 21 22 23 24	MSL1 Preconditioning EXTERNAL VISUAL (EV) PARAMETRIC VERIFICATION (PV) HTRB TC PCT/AC H3TRB IOL ESD DPA Package Physical Dimemsions (PD) RESISTANCE TO SOLDER HEAT (RSH) Solderability THERMAL RESISTANCE (TR) Wire Bond Strength BOND SHEAR Die Shear Summary:	DataSheet Reliability and Characterization Testing Bake 125C Soak 85C, 85% RH IR reflow 260C MIL-STD-750 METHOD 2071 -55C, 25C, 85C, 125C, 150C Ta=150°C or Max Tj, Vd=100%, PER MIL-STD-750-1 Ta=-65C to 150C or Max Tj, PER JESD22A-104 Ta=121°C 15PSIG 100%RH; PER JESD22-A102 Ta=85°C, 85% RH, with 80% Maximum Reverse Bias. JESD22A-101 MIL-STD-750 Method 1037 (N/A for TVS) HBM (AEC-Q101-001) CDM (AEC-Q100-005) AEC Q101-004 SEC. 4 JESD22-B100 JESD22 A-111 (SMD), B-106 (PTH) (260C @30S) J-STD-002; JESD22B102 (245C +0/5S) JESD 24-3, 24-4, 24-6 AS APPROPRIATE MIL-STD-750 METHOD 2037 (JESD22-B116B) AEC-Q101-003 MIL-STD-750 (2017)	Duration / Limits 24 Hrs 168Hrs 3 cycles PER SPEC Operating Range, Per Data Sheet 168 Hrs 500 Hrs 1000 Hrs 168 Cycles 500 Cycles 1000 Cycles 1000 Hrs 168 Hrs 500 Hrs 1000 Hrs 2520 Cycles 15000 Cycles 7560 Cycles PER DATA SHEET PER DATA SHEET PER DATA SHEET PER DATA SHEET PER SPEC 5 Seconds PER SPEC Cpk>1.66 Cpk>1.66	# Failed/Sample Size per Lot SMD only, for Test #7, 8, 9 & 10 All qualificat 0/25 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/77 0/70 0/30 0/30 0/30 0/30 0/10 0/10 0/ min of 5 0/ min of 5	3 Assembly lots stion parts submitted for testing 3 wafer lots 3 wafer lots 3 Assembly lots 3 wafer lots 3 wafer lots 3 wafer lots 1 wafer lot 1 wafer lot 1 Assembly lot	X = Test Needed X X X X X X X X X	Pass/Fail Pass Pass Pass Pass Pass	QBS Test Completed QBS	Pass/Fail Pass